Power off indicator of AP303P was tested upon removing power source(DC or POE).

This indicator need to be about 5ms around between the point when MAX708 is triggered by VCC\_12V(actually 9.6V) failing edge and the Vih of VCC\_3V3\_CPU.

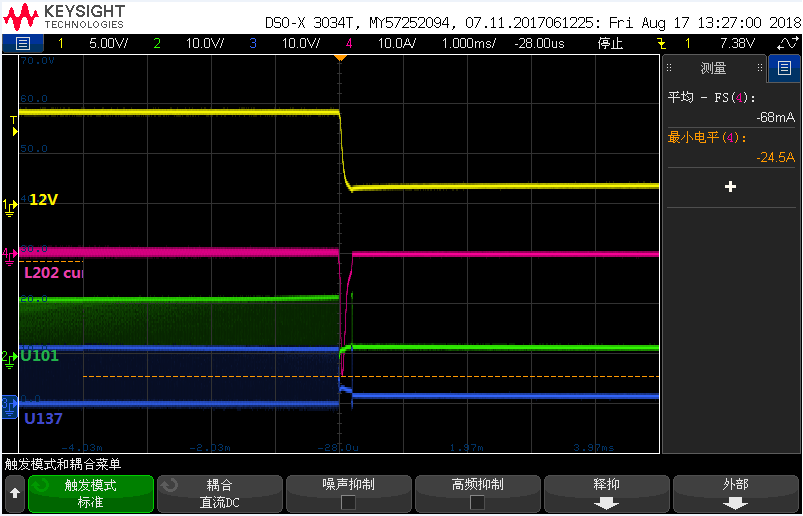
But, this indicator perform as an unstable time period range from 88us to 2.3ms, see pictures below:

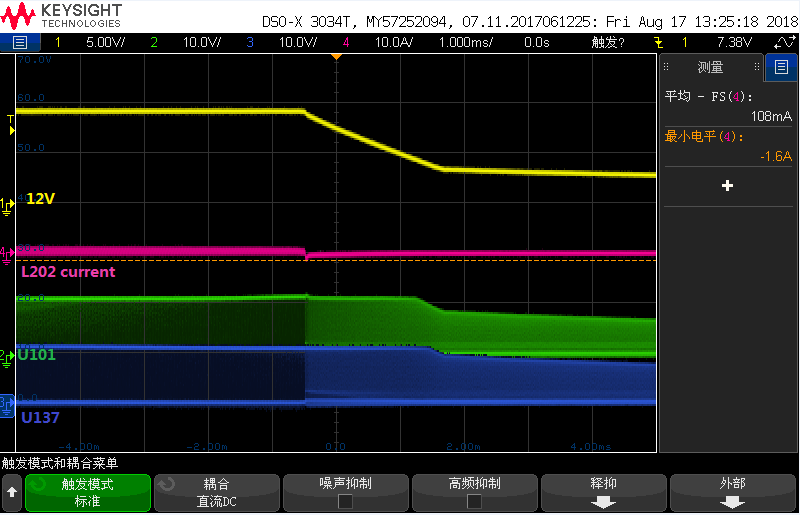




During debugging, we have seen a weird phenomenon that sometimes the current when power off AP303P flow to the reverse direction through L202, but sometimes it does not.

Accordingly, the power off indicator will last a short period of us-level when the current go reverse, and last a longer time of ms-level when the current not. Please see pictures below:





We need to check this issue with TI, and find solution.